



PTO/SB/30 (08-00)

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RCE/2814
#19 RCE
2/19/03
amw

REQUEST FOR CONTINUED EXAMINATION (RCE) TRANSMITTAL Subsection (b) of 35 U.S.C. § 132, effective on May 29, 2000, provides for continued examination of an utility or plant application filed on or after June 8, 1995. See The American Inventors Protection Act of 1999 (AIPA).	Application Number	09/662,358
	Filing Date	09/15/2000
	First Named Inventor	Taiji NODA et al.
	Group Art Unit	2814
	Examiner Name	A.D. Mai
	Attorney Docket Number	740819-423

This is a Request for Continued Examination (RCE) under 37 C.F.R. § 1.114 of the above-identified application.

NOTE: 37 C.F.R. § 1.114 is effective on May 29, 2000. If the above-identified application was filed prior to May 29, 2000, applicant may wish to consider filing a continued prosecution application (CPA) under 37 C.F.R. § 1.53(d) (PTO/SB/29) instead of a RCE to be eligible for the patent term adjustment provisions of the AIPA. See Changes to Application Examination and Provisional Application Practice, Final Rule, 65 Fed. Reg. 50092 (Aug. 16, 2000); Interim Rule, 65 Fed. Reg. 14865 (Mar. 20, 2000), 1233 Off. Gaz. Pat. Office 47 (Apr. 11, 2000), which established RCE practice.

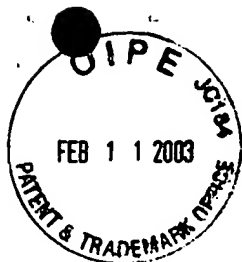
1.	Submission required under 37 C.F.R. § 1.114
a.	<input type="checkbox"/> Previously submitted
i.	<input type="checkbox"/> Consider the amendment(s)/reply under 37 C.F.R. § 1.116 previously filed on _____ (Any unentered amendment(s) referred to above will be entered).
ii.	<input type="checkbox"/> Consider the arguments in the Appeal Brief or Reply Brief previously filed on _____
iii.	<input type="checkbox"/> Other _____
b.	<input checked="" type="checkbox"/> Enclosed
i.	<input checked="" type="checkbox"/> Preliminary Amendment
ii.	<input type="checkbox"/> Affidavit(s)/Declaration(s)
iii.	<input checked="" type="checkbox"/> Information Disclosure Statement (IDS), Form PTO-1449 and Two Cited References
iv.	<input type="checkbox"/> Other _____
2.	Miscellaneous
a.	<input type="checkbox"/> Suspension of action on the above-identified application is requested under 37 C.F.R. § 1.103(c) for a period of _____ months. (Period of suspension shall not exceed 3 months; Fee under 37 C.F.R. § 1.17(l) required)
b.	<input type="checkbox"/> Other _____
3.	Fees The RCE fee under 37 C.F.R. § 1.17(e) is required by 37 C.F.R. § 1.114 when the RCE is filed.
a.	<input checked="" type="checkbox"/> The Director is hereby authorized to charge the following fees, or credit any overpayments, to Deposit Account No. <u>19-2380(740819-423)</u>
i.	<input type="checkbox"/> RCE fee required under 37 C.F.R. § 1.17(e) 02/12/2003 BABRAHA1 00000086 09662358
ii.	<input type="checkbox"/> Extension of time fee (37 C.F.R. §§ 1.136 and 1.17) 01 FC:1801 750.00 OP
iii.	<input type="checkbox"/> Other _____
b.	<input checked="" type="checkbox"/> Check in the amount of \$ <u>750.00</u> enclosed
c.	<input type="checkbox"/> Payment by credit card (Form PTO-2038 enclosed)

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT REQUIRED			
Name (Print/Type)	Donald R. Studebaker	Registration No. (Attorney/Agent)	32,815
Signature		Date	February 11, 2003

CERTIFICATE OF MAILING OR TRANSMISSION			
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner For Patents, Box RCE, Washington, DC 20231, or facsimile transmitted to the U.S. Patent and Trademark Office on:			
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Signature		Date	

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Docket No. 740819-423

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)
Taiji NODA et al.) Group Art Unit: 2814
Serial No. 09/662,358) Examiner: Anh D. Mai
Filed: September 15, 2000)
For: SEMICONDUCTOR DEVICE AND)
METHOD FOR FABRICATING)
THE SAME)

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PRELIMINARY AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Examiner's final Office Action mailed November 18, 2002, please consider the following amendments and remarks in connection with the above-identified application.

IN THE CLAIMS:

Please cancel claim 11 without prejudice or disclaimer to the subject matter disclosed therein.

Please amend the claim 6 as follows:

6. (Twice Amended) A method for fabricating a semiconductor device that includes an extended high-concentration dopant diffused layer of a first conductivity and a pocket dopant diffused layer of a second conductivity, comprising:
a first step of forming a gate electrode over a semiconductor region with a gate insulating film interposed therebetween;